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APPLICANTS

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** CONTINUING DATA ***** *None* *DV*** FOREIGN APPLICATIONS ***** *DV*
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| Foreign Priority claimed <input checked="" type="checkbox"/> yes <input type="checkbox"/> no 35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance Verified and Acknowledged <i>Shuland</i> Examiner's Signature Initials | STATE OR COUNTRY JAPAN | SHEETS DRAWING 33 | TOTAL CLAIMS 20 | INDEPENDENT CLAIMS 3 |
|--|------------------------------|-------------------------|-----------------------|----------------------------|

ADDRESS

22850

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1940 DUKE STREET

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22314

TITLE

Method of manufacturing semiconductor device capable of suppressing impurity concentration reduction in doped channel region arising from formation of gate insulating film

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